

Verification Roadmapping Project

This project is kindly supported by the Electronics-KTN.

What

NMI wishes to help create a Verification Roadmap for the UK semiconductor design industry. The intention is to draw together requirements in the medium-term covering a 3-5 years timeframe from the semiconductor design community (compared to immediate commercial needs and blue-skies 10yr+).

Why

The intention is that a Verification Roadmap document will identify useful collaborations between industry and universities – enabling collaborative projects and their funding routes to be identified.

Earlier Activities

NMI held a pilot round-table discussion in 2006 and a further meeting early in 2009. NMI does not have sufficient internal resources to continue with this project and therefore we have engaged the services of Mike Bartley, a respected verification specialist, to carry this project to a conclusion.

Deliverable

The outcome will be a coherent set of objectives for Verification in the UK, leading to targeted projects and closer cooperation with leading edge design groups, EDA suppliers and academia. This would be presented in a report, for circulation among the participants and available on-line. The outcomes would also be presented at a future NMI event (and summarised at other events as appropriate).

Method of Delivery

A number of round-table meetings at four regional centres in the UK, backed up by telephone interviews as required and a survey. At these meetings, experts will provide their thoughts and opinions on the topic, encompassing expectations and visions for the future and the challenges/barriers involved in getting there.

Timescale

It is expected that an interim report will be completed by Christmas 2009 followed by a review and a final report by the end of January 2010.