



Quality & Reliability Network Event “Cutting Corners Costs Quality”

Book the Date: Wed 24th February 2010
Location: NPL, Teddington

***** FREE to members and invited guests**

Introduction:

NMI is delighted to introduce the 3rd Quality and Reliability network event on the theme “Cutting Corners Costs Quality”. Hosted this year by the National Physical Laboratory (NPL), the event will open with a highly stimulating key note address from **Nihal Sinnadurai (CEO of ATTAC)** and a distinguished Lecturer of the IEEE, on the theme - “**Ticking Time-Bombs in Electronics and Photonics Systems and Networks**”.

As Customer demands on Quality and Reliability become increasingly tough Companies must invest more by "designing in" (DfR) rather than trying to "screen out". The event will deal with a broad cross section of "Quality and Reliability Assurance" issues faced by UK Semiconductor companies. Experienced Industry speakers from companies such as Freescale Semiconductor, NPL, Maser Engineering, DfT Solutions, RCL Ltd., Lauterbach will cover every thing from Integrated Manufacture Automotive Quality through to Analytical supplier case studies.

In addition to our line up of exciting talks we invite you to join in our **Panel debate** around the topic of **Quality Standards** and their applications. Don't miss this opportunity to learn from our Industry experts about the benefits of investing in Quality/Reliability Assurance and why "Cutting Corners Costs Quality".

Event Format:

In addition to the Technical presentations from our Industry Experts we will be providing the following opportunities at the event:

Table Tops: We are delighted to offer delegates the chance to meet a variety of Analytical suppliers from the industry that offer expertise in the area of Quality & Reliability including RCL, DfT Solutions, Reltech, Phasix, Maser ,Dockweiler, Parabilis and Lauterbach

NPL Lab Tour: Our host NPL have kindly provided an excellent opportunity for delegates to tour various different Lab facilities in the NPL site which covers PCBs, High Speed testing, Failure Analysis tools and techniques for IC structures.

As always, there will be ample opportunity during our extended Buffet Lunch break to network with delegates from a variety of organisations ranging from Fabless semiconductor providers, IDMs, Analytical services to Research and academic institutions.

Event host & Sponsor



National Physical Laboratory

Agenda

9:30 am	Registration and Refreshments	
10:00 am	Welcome & Introduction	Paul Jarvie , NMI
10:05 am	Introduction to NPL	Kamal Hossain , NPL
10.15 am	Key note: Ticking Time-Bombs in Electronics and Photonics Systems and Networks Nihal Sinnadurai, CEO of ATTAC Distinguished Lecturer of the IEEE	
11:05 am	Designing in Automotive Quality	Ally Gorman , Freescale Semiconductor
11:35 am	Coffee Break	
11:50 am	Surface Analysis, PCB & Component Reliability	Alex Shard / Chris Hunt , NPL
12.20 am	Taking the right decisions	Barry Lock , Lauterbach Ltd
12.40 am 1:25 pm	Buffet Lunch + Table Top Exhibitions NPL Lab Tours	
2:35 pm	Applying the principles of DfX to correct design weaknesses found on first silicon	Stephen Meats , RCL / Eric Cormack , DfT Solutions
3.00 pm	Obsolete products application, a potential reliability hazard.	Mark Gortemaker , Maser Engineering
3:30 pm	Coffee Break	
3:45 pm	Managing the Automotive standard TS16949	Arthur David , Industry Forum
4:15 pm	Panel Session: Do the Standards fit the application ? Chair: Nihal Sinnadurai (CEO of ATTAC) Guests: NPL, Freescale Semiconductors, DfT Solutions, NXP	
4:45 pm	Wrap Up & Close	

For On-line Registration :
www.nmi.org.uk

For Table Top availability :
Call : +44(0)77394 27766
Email : paul.jarvie@nmi.org.uk

About the NMI...

The NMI is the semiconductor trade body of the UK and Ireland. Our mission is to build and support a strong semiconductor community within the region and act as facilitator and catalyst for sector development. The NMI is a not-for-profit organisation funded by its members and includes fabless semiconductor companies, intellectual property providers, contract design services, integrated device manufacturers, semiconductor manufacturers, vendors, service providers, research and academic institutions.

Key work streams include:

- Encouraging innovation, communication and business collaboration through sector networks.
- Supply chain efficiencies through benchmarking and best practice.
- Information provision.
- Representation to government, policy makers and regulators.
- Skills development, education and training.

See www.nmi.org.uk and click on events for full schedule and latest information

Map and directions:

NPL Address

National Physical Laboratory
Hampton Road
Teddington
Middlesex
TW11 0LW

E-mail: enquiry@npl.co.uk

Telephone: +44 20 8977 3222 (Switchboard)

Directions to NPL.... [Click this link](#) or go to www.npl.co.uk

Transport

Buses 281, 285, R68, 33, 481 and X26 serve Teddington.

Trains from London Waterloo leave regularly, journey time is approx. 30 mins

Map

