



**Embedded Systems Network  
Multi-Core/Multi-Processor Systems**

Thursday 25th June 2009

Renesas Technology Europe, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH

NMI network events provide opportunities for professionals to meet, exchange views and debate the issues.

**Event Theme**

Multi-Core & Multi-Processor systems have been gaining ground for a number of years. Now with the complexity of such systems rapidly increasing, the ending of the 'free lunch' delivered by successive generations of silicon technology and the need for performance gains coupled with energy efficiency, what is the way forward?

This event will look at the challenges in designing, verifying and debugging a system with multiple processors. How can you ensure reliability, particularly if the multiple processors are from different vendors rather than on a single chip? Is it always necessary to use multiple cores to get performance? Do designers need to be aware of the underlying system?

In addition to the presentations, as with all NMI network events, there will be the opportunity to meet and network with attendees from a variety of organisations ranging from fabless semiconductor providers, IDMs, tool suppliers, foundries, design/software service providers, IP providers, training, research and academic institutions.

**Agenda**

09:00 Registration

*Refreshments, Networking & Sponsor Table-Tops*

*"Multicore System Architecture - from the Software Perspective"*  
**Paul Tingey, Wind River**

9:45

*Welcome & Introduction*

**Robin Kennedy, NMI**  
**Jeremy Bennett, Embecosm (Chair)**

12:30 – 14:00 Lunch & Networking

*"What Do Embedded Software Developers Really Think About Multicore Architectures?"*  
**David Stewart, Critical Blue**

*"Fifteen Years in Multi-Core Systems"*

**Andy Lunness, STMicroelectronics**

*"Systems Knowledge at a Premium"*  
**Joe Brennan, Renesas**

*"Surviving a Multicore Development Project"*

**Barry Lock, Lauterbach**

*"Evolution of Protocol Aware Exercisers using multi-threaded techniques"*

**Matthew Dunn, LeCroy**

11:00 -11:30 Break

*"Multi-Core, Multi-threaded, Multi-standard: Using architecture to maximise IP product scalability"*

**Tony King-Smith, Imagination Technologies**

16:00 Close

*Refreshments & Continued Networking*

*The NMI Embedded Network is sponsored by :*



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