

NMI Networks

“FPGA Innovations - Lifting the Lid”

Wednesday 22nd February 2012 National Instruments, Newbury

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FURTHER DETAILS AND ON-LINE REGISTRATION AT www.nmi.org.uk/events

09:30 - 10:00 Registration, Refreshments, Table-top exhibitions

Welcome - Robert Morton, Managing Director National Instruments UK and Ireland
NMI Welcome & Introduction – Jon Older

Paul Dillien – High Tech Marketing

- Selecting an FPGA - Considering density, features and cost

Ian Bell – National Instruments

- Introduction to Graphical Programming for FPGAs

11:20 Break, networking and exhibitor discussions

Andy Jolley - Synopsys

- High Reliability Implementation Techniques for FPGA Designers

David Wood - EDASIP

- Heuristic Design just got smarter

13:00 Lunch, networking and exhibitor discussions

John Adair - Enterpoint

- Making PCI-Express work in FPGAs

Martin Terry - Infineon

- Emulating large complex SOC devices in a Xilinx FPGA environment

15:00 Break, networking and exhibitor discussions

Robert Jeffery - Mentor Graphics

- Stuck in the lab fixing bugs? Maybe it's time to revisit your verification process

John Barnard - Telexsus

- My FPGA has to go on a board! – Using JTAG for FPGA board validation and programming

Discussion

- "Key FPGA Challenges"

16:30 Close

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